



Silicon Wafer NA TC Chapter

Spring Standards Meetings
Tuesday, March 26, 2024
9:00 am - 12:00 noon Pacific
SEMI HQ/Online

AGENDA

1.0 Welcome / Call to Order

- 1.1 Introductions
- 1.2 Meeting Reminders (*Membership Requirement, Antitrust and Intellectual Property Reminders, Effective Meeting Guidelines*)
- 1.3 Agenda Review

2.0 Review of Schedule for the next meeting

3.0 Review and Approval of Previous Meeting Minutes (July 11, 2023)

4.0 Liaison Report

- 4.1 European Committee
- 4.2 Japan Committee

5.0 Staff Report

6.0 Regulations Change Report (if applicable)

7.0 Ballot Review

8.0 Task Force Reports

- 8.1 Int'l Advanced Wafer Geometry Task Force /Noel Poduje (SMS)
- 8.2 Int'l Automated Advanced Surface Inspection Task Force/ Kurt Haller (KLA)
- 8.3 Int'l SOI Wafers TF/Gerd Pfeiffer (SOITEC)
- 8.4 Int'l Test Methods TF/Dinesh Gupta (STA)
- 8.5 Int'l Polished Wafers (Substrates) TF/TBD

9.0 Old Business

10.0 New Business

- 10.1 5 Year Review

11.0 Action Item Review

- 11.1 Open Action Items
- 11.2 New Action Items

12.0 Next Meeting and Adjournment

- 12.1 The next meeting of the NA Silicon Wafer committee, July 9, 2024 Moscone Center, San Francisco, CA.