



EuroCDP
EU Chips Design
Platform

EU Chips Design Platform: A catalyst for fabless startups in Europe

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“AI Chip Design: Positioning Europe in the Global Supply Chain”
Semicon Europe, Munich, November 19, 2025

IC design context in EU

**~30% of
global IC startups
are hosted in
Europe¹**

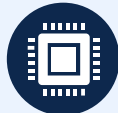
But they attract
**only ~6% of
global startup
funding.**

Talent and
research are not
the bottleneck —
access to venture
infrastructure is.



What IC Startups and SMEs need

-  Design represents **~33%** of the total WW semiconductor value chain.
-  Fabless SMEs are deemed essential for resilience and innovation in EU.
-  Escalating innovation cost
-  Hardware seen as high-risk by investors



Technical support to reach
prototyping and MVPs



Affordable EDA tools, IP and
design enablement



Integrated mentoring
and business acceleration



Continuity of funding from seed to growth
with non-dilutive options

EuroCDP Objectives

Grow IC design in Europe

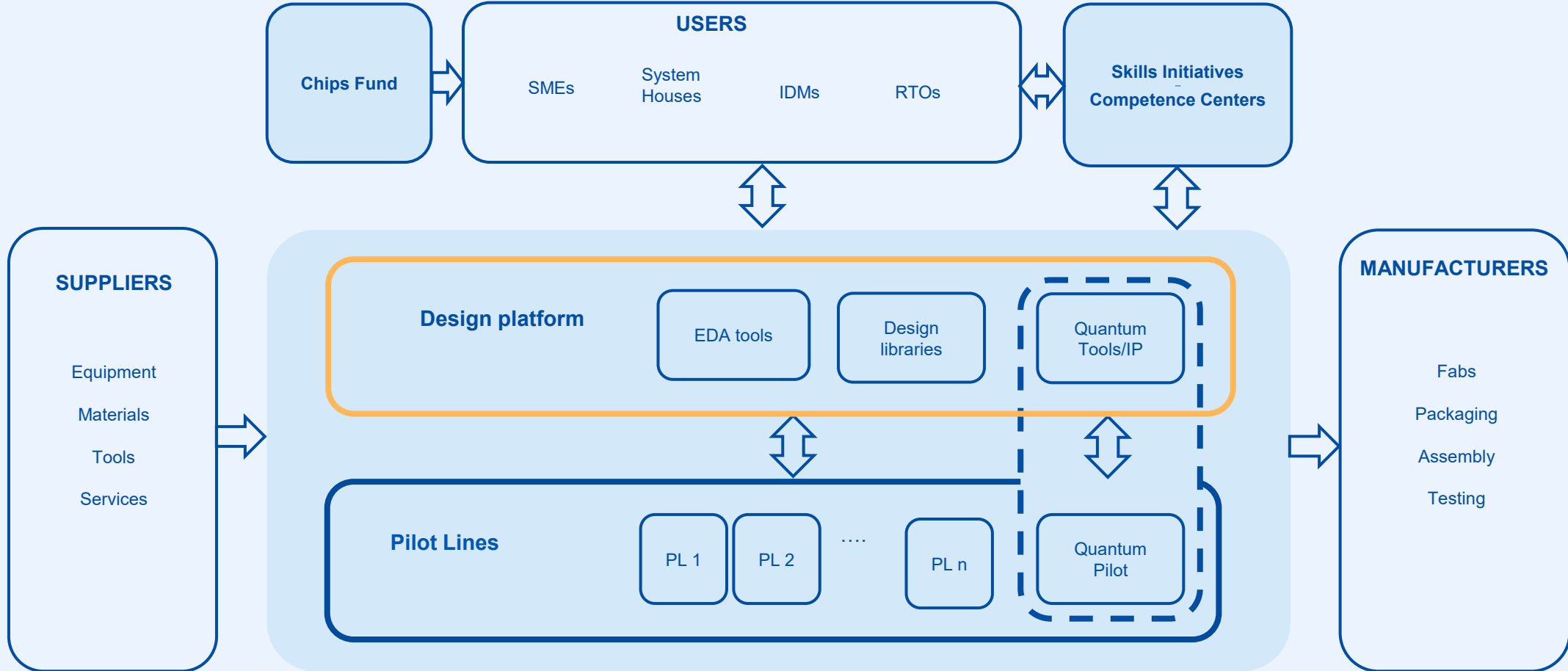


- Lower the barriers for IC design (specifically for adv. tech.)
- Foster collaboration among EU stakeholders
- Support the development of IC design skills by offering training and support services
- Integrate access to pilot lines and foundries



An integrated platform offering all essential services in one place

At the heart of EU Chips act – pillar 1



EuroCDP Value Proposition for EU Fabless

Chip Design Support



Technical training access



Cloud-based **Computing & Storage capacities**



Open source **IP & EDA**



Sandbox for exploration

SME Financial Support



Users' project **grants**

IP & EDA

infrastructure

DET services



Negotiated Commercial EDA & IP licenses

Entrepreneurship Support



Mentoring



Incubation program to enable start-ups ideas to take root and grow

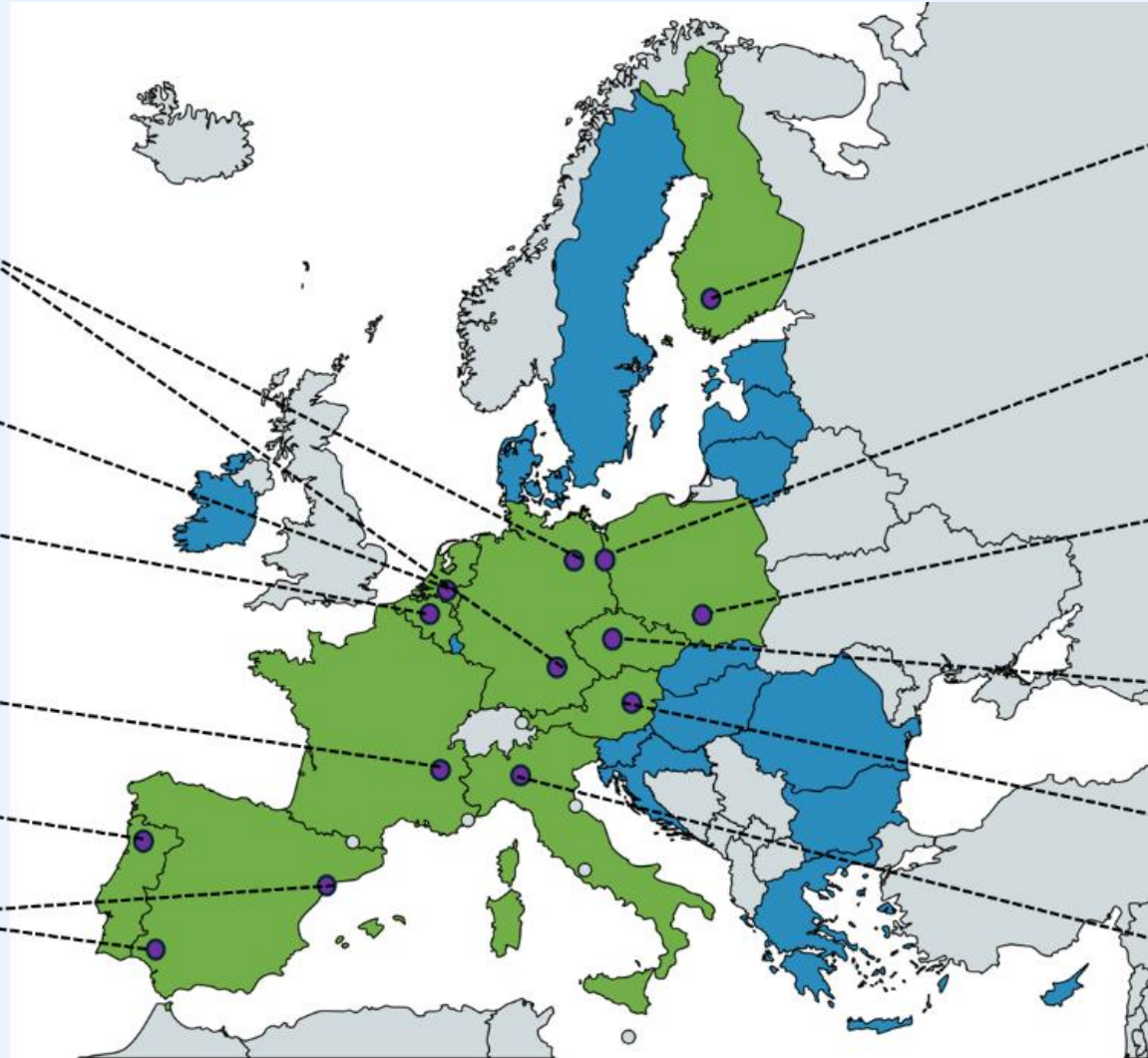


Accelerator program to boost start-ups to scale

EuroCDP at a glance

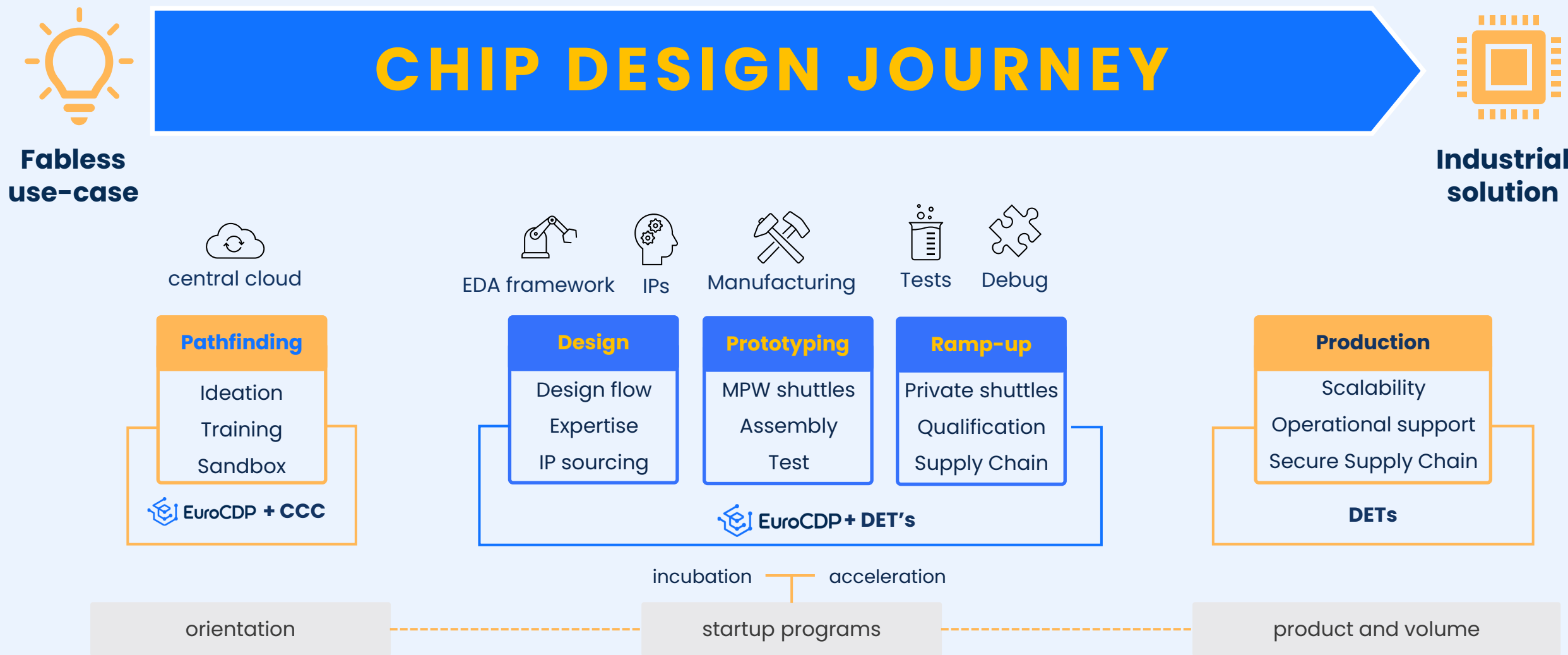


The PCT consortium

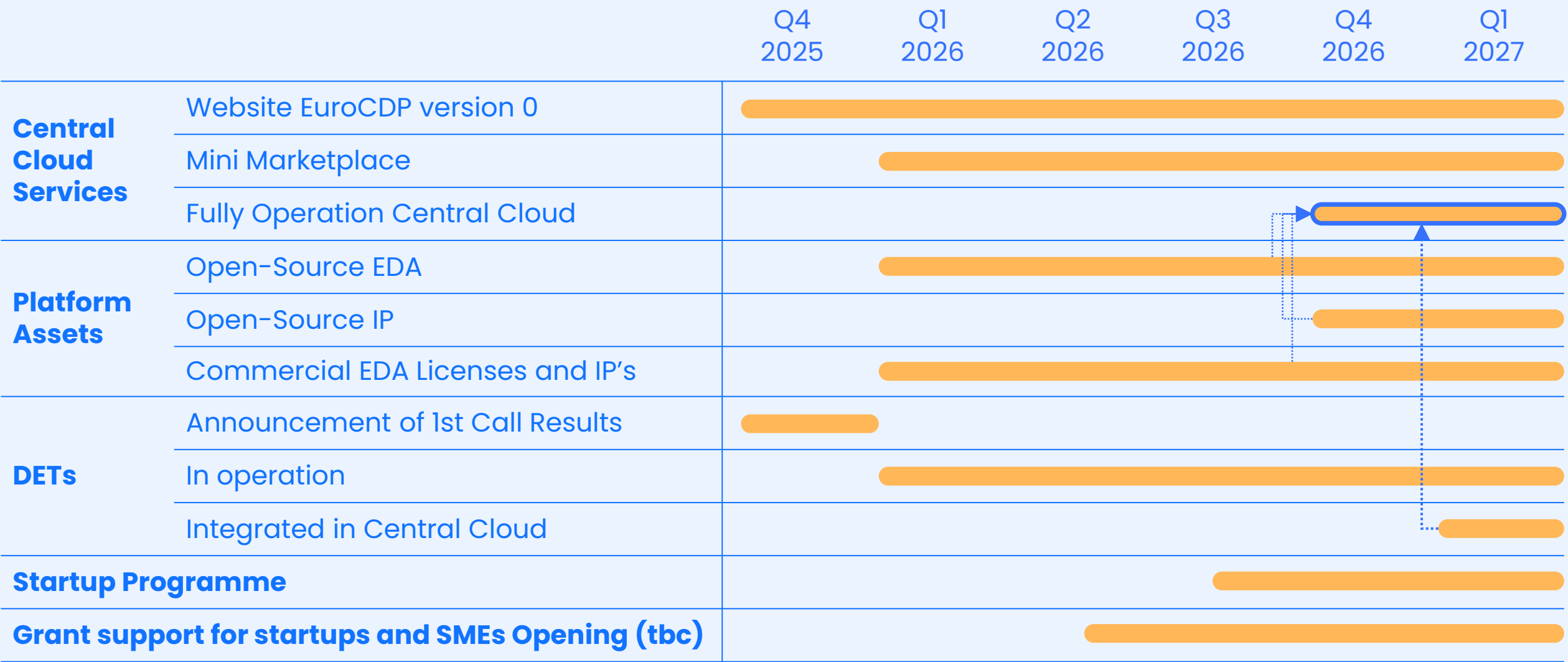




EuroCDP user journey



EuroCDP timeline



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The **EU Chips Design Platform** has landed in Mechelen for the launch 🚀 of **FC3 - Flanders Chips Competence Center**. Great to share the floor with **NanoIC pilot line**, **Europractice**, **FAMES Pilot Line**, **APECS Pilot Line**, **WBG Pilot Line** | future **power electronics** and **PIXEurope**. Looking forward to serving the Flemish ecosystem.



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Maarten Van Rompuy and 162 others

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 EuroCDP

EU Chips Design Platform

Your trusted partner in next-gen chip design

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The EU Chips Design Platform is being built to become Europe's virtual entry point to chip innovation.

Once launched, it will offer seamless access to design tools, IP, manufacturing and testing services, expert support, training, and funding – all in one place. By streamlining access and offering shared, pre-negotiated resources, the platform will help reduce the economic and technical barriers of chip design – especially for European startups and SMEs.

From idea to chip – faster, easier

Follow our progress and join the community shaping the future of European chip design.

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From idea to chip — faster, together.